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01/10/02

50-550

Formal Drawings (4 sheets) set

U.S. UTILITY Patent Application

SEP 09 2003

PAT

APPL NUM 10043682	FILING DATE 01/10/2002	CLASS 365	SUBCLASS 365	EXAMINER VAN THU NGUYEN
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** APPLICANTS: Cowles Timothy; Shore Michael; Mullarkey Patrick;

** CONTINUING DATA VERIFIED:

Y/VTN

THIS APPLICATION IS A DIV OF 09/803,265 03/08/2001

Van Thu Nguyen

** FOREIGN APPLICATIONS VERIFIED:

W/VTN

PG-PUB DO NOT FORGUS: <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO. 500792.05
Foreign priority claim: <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	35 USC 119 conditions met: <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	
Verified and Acknowledged examiner's initials: VTN		

TITLE: Refresh controller and address remapping circuit and method for dual mode full/reduced density arrays

49(1) Formal Drawings (4 sheets) set			1/10/02	
DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg. 4	Figs. Drwg. 5	Print Fig. 4	Total Claims 8	Print Claim for O.G. 1
(Assistant Examiner) _____ (Date) _____ VAN THU NGUYEN PATENT EXAMINER Van Thu Nguyen (Primary Examiner) 10/10/03 (Date) (Legal Instruments Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED	
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